



PATENT

ifw

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: Kwan-Yong LIM, et al.

Serial No.: 10/616,302

Group Art Unit: 2815

Filed: July 10, 2003

Examiner: Jackson Jr., Jerome

For: STACK GATE ELECTRODE SUPPRESSED WITH INTERFACE-REACTION AND  
METHOD FOR FABRICATING SEMICONDUCTOR DEVICE HAVING THE  
SAME

\* \* \* \* \*

RESPONSE TO RESTRICTION REQUIREMENT

Honorable Commissioner for Patent  
P.O. Box 1450  
Alexandria, VA 22313-1450

May 17, 2004

Sir:

In response to the Request for Restriction mailed April 20, 2004 (Paper No. 041404), Applicant elects Group II, claims 6-10, drawn to a process, classified in class 438, subclass 15+, with traverse.

Applicant respectfully traverses the restriction requirement. The claims of Group I (Claims 1-5) drawn to a product and the claims of Group II (Claims 6-10) drawn to a process are closely related to each other and define a single invention. The product defined in claims 1-5 is manufactured by the process of claims 6-10, and thus, claims 1-10 define a single inventive concept. Further, Applicant respectfully submits that it would not be unduly burdensome for the Examiner to examine the claims of both groups. Applicant respectfully requests that claims 1-10 be examined.

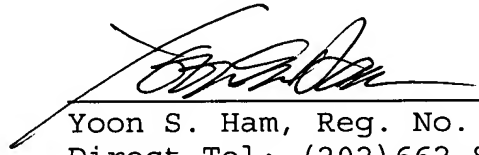
May 17, 2004

Serial No.: 10/616,302  
Atty. Docket No.: P68987US0

An early Office Action on the merits is respectfully requested.

If a Petition for Extension of Time is necessary and the petition and/or payment of the fee(s) is not enclosed, this will act as a petition to extend the time for response and an authorization to charge the necessary fee(s) under 37 CFR 1.17(a)-(d) to Deposit Account 06-1358.

Respectfully submitted,



---

Yoon S. Ham, Reg. No. 45,307  
Direct Tel: (202) 662-8483

JACOBSON HOLMAN PLLC  
The Jenifer Building  
400 Seventh Street, N.W.  
Washington, D.C. 20004  
Main Tel: (202) 638-6666

YSH:dj